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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Matthew J. Adiletta, et al.      Art Unit : Unknown  
Serial No. : 09/387,109      Examiner : Unknown  
Filed : August 31, 1999  
Title : SDRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents  
Washington, D.C. 20231

PETITION TO REVIVE APPLICATION UNDER 37 CFR §1.137(b)

Applicant hereby petitions under 37 CFR §1.137(b) to revive the above application, which was abandoned on September 8, 2000 for failure to respond to the Notice to File Missing Parts mailed September 24, 1999.

Enclosed is 1) a signed Combined Declaration and Power of Attorney; 2) a signed Assignment, Recordation Form cover sheet and \$40 fee; 3) a Response to Notice to File Missing Parts w/accompanying checks for \$740 and \$130; and 4) a check for \$1280 in payment of the petition fee by a large entity as set forth in 37 CFR §1.17(m).

Applicant submits that the entire period of delay was unintentional.

Please apply any other charges or credits to Deposit Account No. 06-1050.

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

May 21, 2002

Date of Deposit

Signature

*Jennifer Hess Carleton*

Jennifer Hess Carleton

Typed or Printed Name of Person Signing Certificate

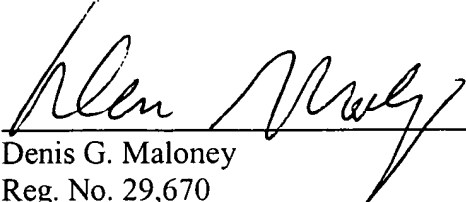
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Respectfully submitted,

Date: 5/21/02

  
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